

**IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE**

Appl. No. : 10/538,369
Applicants : Geoffrey F. BURNS et al.
Filed : 13 June 2005

TC/A.U. : 2183
Confirmation : 6028
Examiner : Jacob Andrew PETRANEK

Atty. Docket : US-020543

Title: MODULATOR INTEGRATION OF AN ARRAY
PROCESSOR WITHIN A SYSTEM ON CHIP

AMENDMENT and/or RESPONSE under 37 C.F.R. § 1.112

U.S. Patent and Trademark Office
Customer Window, Mail Stop **Amendment**
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Sir:

In response to the non-final Office Action of 21 March 2008, please amend the above referenced application as follows and reconsider the application in light of the following remarks.

This paper includes (each beginning on a separate sheet):

- 1. Amendments to the claims;**
- 2. Amendments to the drawing figures;**
- 3. Remarks/Discussion of issues;**
- 4. Attached replacement drawing sheets.**